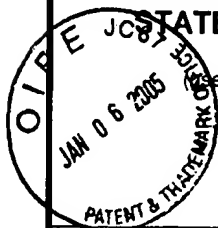


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				Filing Date	11/03/00
				First Named Inventor	Enrique Musoll Adolfo M. Nemirovsky Mario D. Nemirovsky Narendra Sankar
				Group Art Unit	2183
				Examiner Name	Shane Gerstl
Sheet	1	of	2	Attorney Docket Number	MIPS.0170-00-US



U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ²			
EC	AA	5699537		Sharangpani et al	12/16/1997	
EC	AB	5724565		Dubey et al	03/03/1998	
EC	AC	5745778		Robert A. Alfieri	04/28/1998	
EC	AD	6105053		Kimmel et al	08/15/2000	
EC	AE	6141746		Kawano et al	10/31/2000	
EC	AF	6219780		Mikko Herman Lipasti	04/17/2001	
EC	AG	6343348		Tremblay et al	01/29/2002	
EC	AH	6378063		Corwin et al	04/23/2002	
EC	AI	6542991		Joy et al	04/01/2003	
EC	AJ	6691221		Joshi et al	02/10/2004	

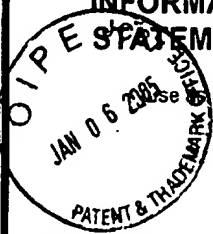
FOREIGN PATENT DOCUMENTS						
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				Group Art Unit	2183
				Examiner Name	Shane Gerstl
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Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
EC	BA	<i>The PowerPC Architecture: A Specification for a New Family of RISC Processors</i> , Second Edition, Morgan Kaufmann, San Francisco, pp.70-92 (May 1994)		
EC	BB	<i>MC68020 32-Bit Microprocessor User's Manual</i> , Third Edition, Prentice Hall, New Jersey, pp. 3-125, 3-126, and 3-127 (1989)		
EC	BC	Potel, M.J., "Real-Time Playback in Animation Systems," <i>Proceedings of the 4th Annual Conference on Computer Graphics and Interactive Techniques</i> , San Jose, CA, pp. 72-77 (1977)		
EC	BD	<i>ARM Architecture Reference Manual</i> , Prentice Hall, pp. 3-41, 3-42, 3-43, 3-67, 3-68 (1996)		
EC	BE	<i>ESA/390 Principles of Operation</i> , IBM Library Server, Table of Contents and Para.7.5.31 and 7.5.70 (1993) (available at: http://publibz.boulder.ibm.com/cgi-bin/bookmgr_OS390/BOOK/DZ9AR001/CCONTENTS)		
EC	BF	<i>MC88110 Second Generation RISC Microprocessor User's Manual</i> , Motorola, Inc., pp.10-66, 10-67, and 10-71 (1991)		
EC	BG	Diefendorff, Keith et al., "Organization of the Motorola 88110 Superscalar RISC Microprocessor," <i>IEEE Micro</i> , Vol, 12, No. 2, pp.40-63 (1992)		
EC	BH	Kane, Gerry, <i>PA-RISC 2.0 Architecture</i> , Prentice Hall, New Jersey, pp. 7-106 and 7-107 (1996)		
EC	BI	Diefendorff, Keith et al, "Altivec Extension to PowerPC Accelerates Media Processing," <i>IEEE Micro</i> , Vol, 20, No. 2, pp.85-95 (March-April 2000)		
EC	BJ	Diefendorff, Keith, "WinChip 4 Thumbs Nose at ILP," <i>Microprocessor Report</i> , Vol, 12, No.16, 10 pages (December 7, 1998)		
EC	BK	Gwennap, Linley, "Digital 21264 Sets New Standard," <i>Microprocessor Report</i> , Vol, 10, No. 14, 11 pages (October 28, 1999)		
EC	BL	Diefendorff, Keith, "Power4 Focuses on Memory Bandwidth," <i>Microprocessor Report</i> , Vol, 13, No. 13, 13 pages (October 6, 1999)		
EC	BM	Diefendorff, Keith, "K7 Challenges Intel," <i>Microprocessor Report</i> , Vol, 12, No. 14, 7 pages (October 26, 1998)		

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